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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Complete if Known

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Application Number

09/873.524

Filing Date

6/4/01

First Named Inventor

Chiuss

Group Art Unit

2663

Examiner Name

Not yet assigned

Attorney Docket Number

Chiussi 20-8

Center 2600

K. GEORGE

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

Examiner
Signature

Date _____

Considered

18 MAR 05

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449B/PTO		Complete if Known		RECEIVED SEP 04 2001 Center 260C
INFORMATION DISCLOSURE STATEMENT BY APPLICANT AUG 30 2001 (use as many sheets as necessary)		Application Number	09/873,524	
		Filing Date	6/4/01	
		First Named Inventor	Chiussi Technology	
		Group Art Unit	2661	
		Examiner Name	Not yet assigned	
		Attorney Docket Number	20-8	
Sheet	of			

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
22		JENNIFER REXFORD, FLAVIO BONOMI, ALBERT GREENBERG, ALBERT WONG, "Scalable Architectures for Integrated Traffic Shaping and Link Scheduling in High-Speed ATM Switches"	
22		H. JONATHAN CHAO, JUN S. HONG, "Design of an ATM Shaping Multiplexer with Guaranteed Output Burstiness", Dept. of Electrical Engineering, Brooklyn, New York, August 28, 1996	
22		DIMITRIOS STILIADIS, ANUJAN VARMA, "A General Methodology for Designing Efficient Traffic Scheduling and Shaping Algorithms", 1997 IEEE	

Examiner Signature	<i>[Signature]</i>	Date Considered	18 MAR 05
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